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Title of Invention:	METHODS FOR DEBUGGING SCAN TESTING FAILURES OF INTEGRATED CIRCUITS	
First Named Inventor/Applicant Name:	Amar Guettaf	
Customer Number:	26111	
Filer:	Michael David Specht/Maya Bennett	
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1 1875445	18754450000COC.pdf	115787	ves	4	
		'	fda5ac1c325e10b4fbcf0b4633c67f962f194 65e	'	

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Miscellaneous Incoming Letter	1	1
	Request for Certificate of Correction	2	4
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.